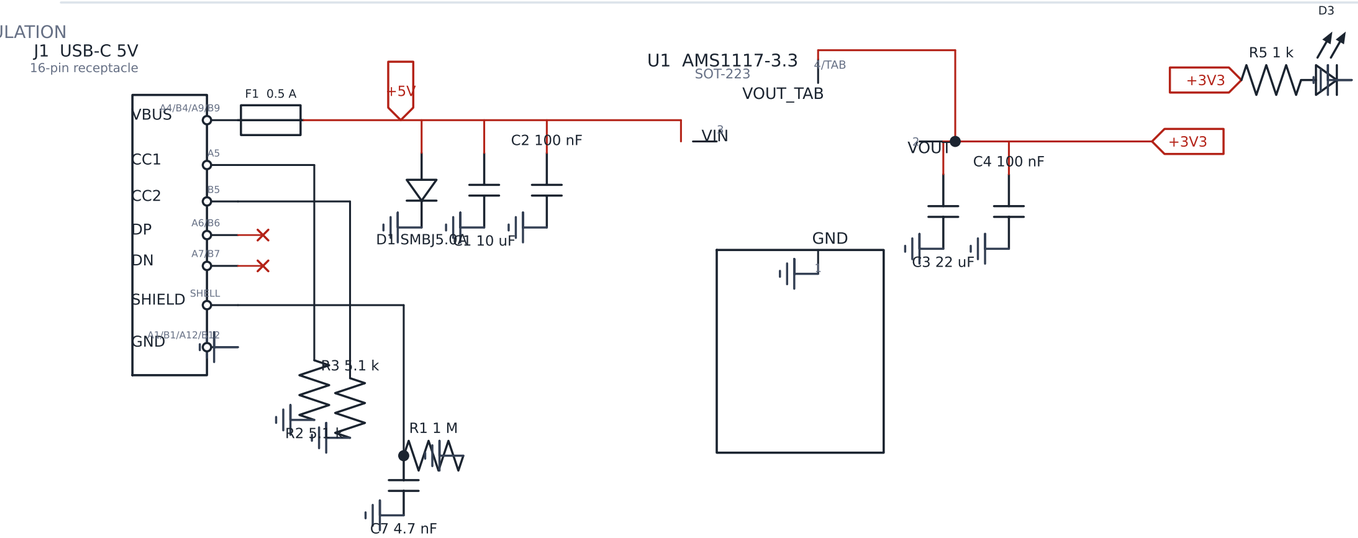
USB-C 5 V / 3.3 V LVTTL TO RS-485

SN65BC184 • half-duplex • non-isolated • AMS1117-3.3 SOT-223

SHEET 01
REV B • 2026-11-09

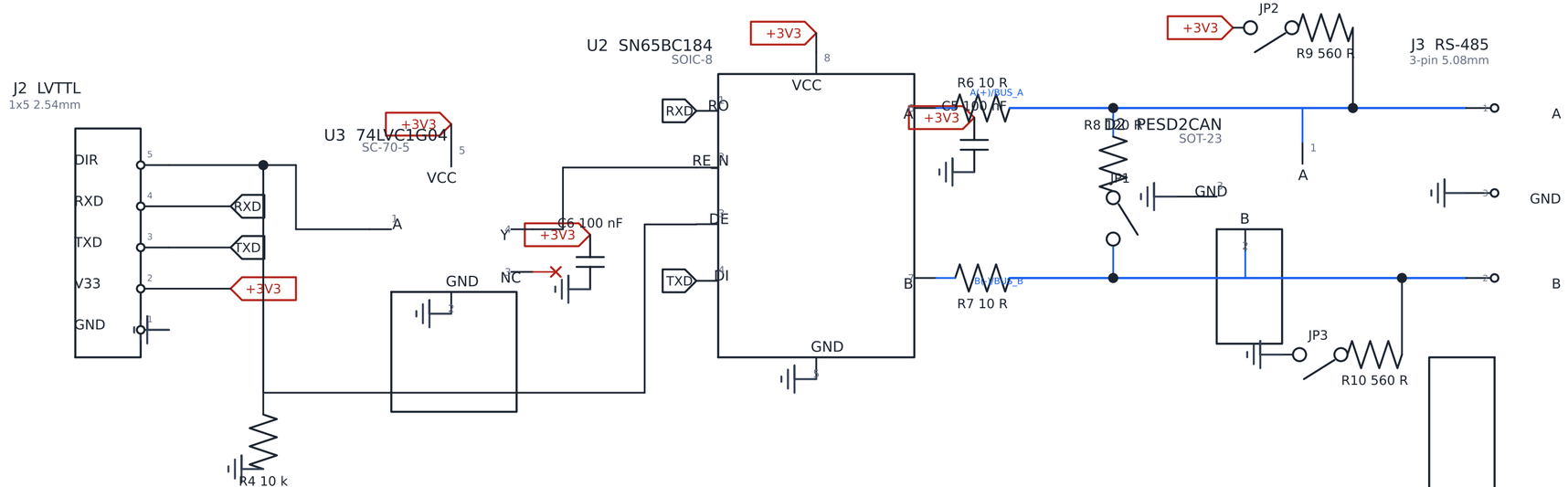
USB-C INPUT / PROTECTION / 3.3 V REGULATION



3.3 V LVTTL INTERFACE / DIRECTION CONTROL

SN65BC184 TRANSCEIVER

RS-485 BUS / PROTECTION / TERMINAL



DESIGN NOTES

1. DIR=0: receive (/RE=0, DE=0). DIR=1: transmit (/RE=1, DE=1). R4 defaults DIR low.
2. Fit R8/JP1 termination only at both physical cable ends.
3. Fit R9/JP2 and R10/JP3 bias links at one master only.
4. AMS1117 SOT-223: 1=GND, 2=VOUT, 3=VIN, TAB=VOUT. Solder TAB to copper.