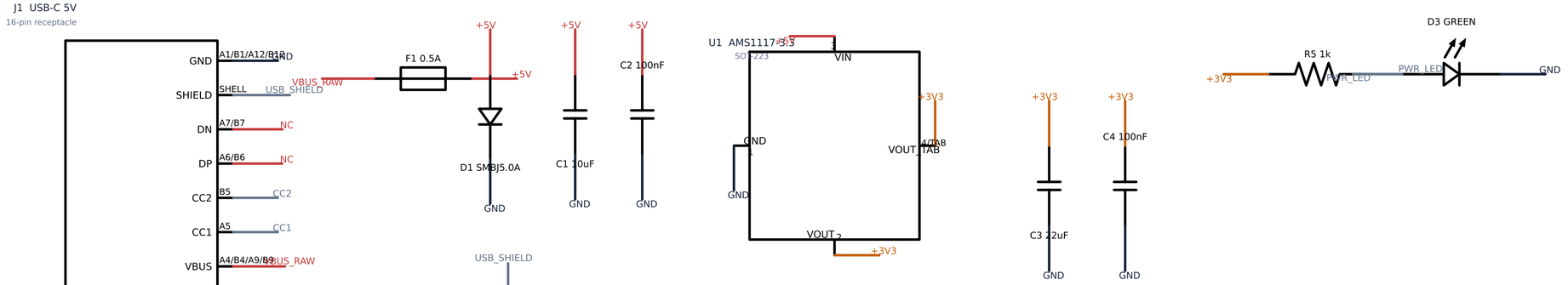
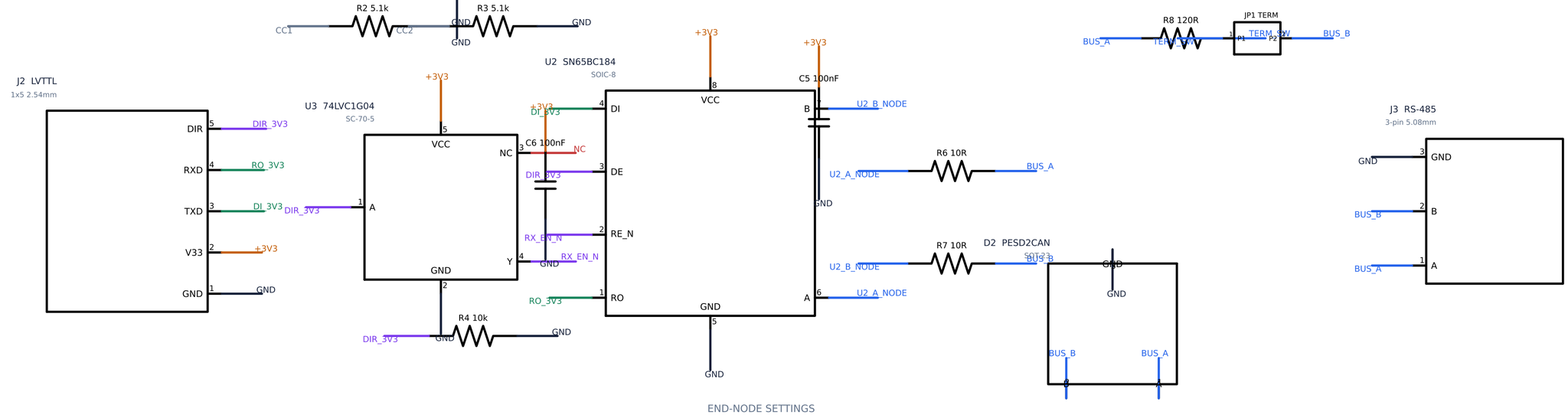
USB-C 5 V / 3.3 V LVTTL to RS-485

SN65BC184 | half-duplex | non-isolated | equal net labels are connected

POWER INPUT / 3.3 V REGULATOR



3.3 V LOGIC / DIRECTION CONTROL / RS-485 BUS



END-NODE SETTINGS

DESIGN NOTES

1. DIR=0: DE=0 and /RE=0 (receive). DIR=1: DE=1 and /RE=1 (transmit). R4 defaults DIR low.
2. Fit 120 ohm termination only at both physical cable ends. Fit JP2+JP3 at exactly one biased master.
3. USB-C is a 5 V sink (CC1/CC2 each have 5.1 kohm Rd); D+/D- are unused. Signal ground is required.
4. AMS1117 SOT-223: Pin 1=GND, Pin 2=VOUT, Pin 3=VIN; TAB is also VOUT and must be soldered.
5. Review the exact SN65BC184 order code, pinout, package, and data rate before PCB release.